

A Step-wise Development Method with Progress Concerns

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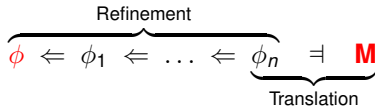
Formal Systems Development using Refinement

- To develop a system **M** satisfying property ϕ , i.e., $\mathbf{M} \models \phi$.
 - **M**: some transition system
 - ϕ : some logical formula
- The main challenge: the **complexity of the system**.
- **Refinement** allows the step-by-step design of the system.

Refinement

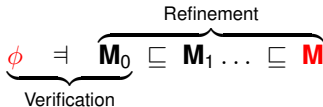
The UNITY way vs. the Event-B way

- UNITY: Refines the **formulae**.



- Cons: **Hard to understand** the choice of refinement.

- Event-B: Refines **transition systems**.

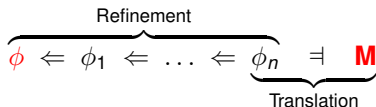


- Cons: No support for **liveness properties**.

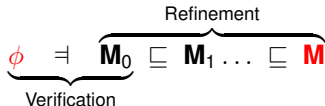
Refinement

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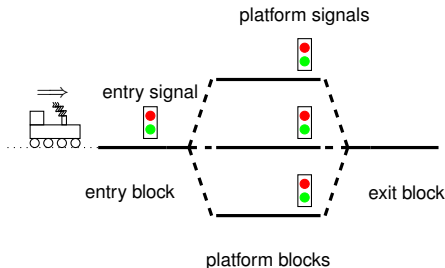
The Unit-B Modelling Method

- Inspired by UNITY and Event-B.
- Support the reasoning of **liveness properties** (UNITY).
- **Refinement** of transition systems (Event-B style).
- Developments using Unit-B are **guided by both safety and liveness requirements**.

Outline

- 1 Formal Systems Development using Refinement
- 2 The Unit-B Modelling Method**
 - Unit-B Models
 - Properties of Unit-B Models
 - Refinement
- 3 Summary

Running Example. A Signal Control System



SAF 1 There is at most one train on each block

LIVE 2 Each train in the network eventually leaves

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Unit-B Models – Discrete Transition Systems

- States are captured by **variables v** .
- Transitions are modelled by **guarded and scheduled events**.

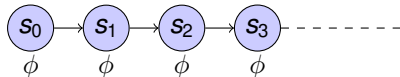
Traces and the Language of Temporal Logic

A trace σ is a (finite or infinite sequence of states)

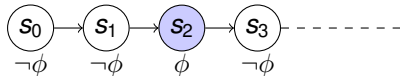
$$\sigma = s_0, s_1, s_2, s_3, \dots$$

- A (basic) **state formula** P is any **first-order logic formula**,
- The basic formulas can be extended by **combining** the **Boolean operators** ($\neg, \wedge, \vee, \Rightarrow$) with **temporal operators**:

always: $\Box \phi$



eventually: $\Diamond \phi$



Guarded events

e

```
any t where
  G.t.v
then
  S.t.v.v'
end
```

- t : parameters
- $G.t.v$: guard
- $S.t.v.v'$: action

- $e.t$ is **enabled** when $G.t.v$ holds.
- Execution of $e.t$: v is **updated** according to the action $S.t.v.v'$.
- $e.t$ corresponds to a formula $act.(e.t)$.

Scheduled events (1/2)

e

any t where

...

during

$C.t.v$

upon

$F.t.v$

then

...

end

- $C.t.v$: coarse-schedule.

- $F.t.v$: fine-schedule.

- Healthiness condition:

$$C.t.v \wedge F.t.v \Rightarrow G.t.v$$

Liveness (Scheduling) Assumption

If $C.t.v$ holds infinitely long and $F.t.v$ holds infinitely often then eventually $e.t$ is executed.

$$sched.(e.t) = \square(\square C \wedge \square \diamond F \Rightarrow \diamond(F \wedge act.(e.t)))$$

Scheduled events (1/2)

e

any t where

...

during

$C.t.v$

upon

$F.t.v$

then

...

end

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Schedules vs. Fairness

$e \hat{=} \text{any } t \text{ where } G.t.v \text{ during } C.t.v \text{ upon } F.t.v \text{ then } \dots \text{ end}$

- Schedules are a **generalisation** of weak- and strong-fairness.
- Weak-fairness:
 If e is **enabled infinitely long** then e eventually occurs.
 - Let C be G and F be \top .
- Strong-fairness:
 If e is **enabled infinitely often** then e eventually occurs.
 - Let F be G and C be \top .

Scheduled events (2/2)

Conventions

$e \hat{=} \text{any } t \text{ where } \dots \text{ during } C.t.v \text{ upon } F.t.v \text{ then } \dots \text{ end}$

- **Unscheduled** events (without **during** and **upon**): C is \perp
- When only **during** is present (no **upon**), F is \top .
- When only **upon** is present (no **during**), C is \top .

Execution of Unit-B Models

$$ex.M = saf.M \wedge live.M \quad (1)$$

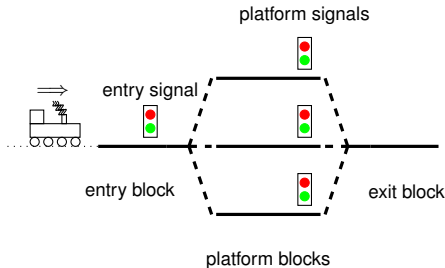
$$saf.M = init \wedge \square step.M \quad (2)$$

$$step.M = (\exists e.t \in M \cdot act.(e.t)) \vee SKIP \quad (3)$$

$$live.M = \forall e.t \in M \cdot sched.(e.t) \quad (4)$$

$$sched.(e.t) = \square(\square C \wedge \square \diamond F \Rightarrow \diamond(F \wedge act.(e.t))) \quad (5)$$

A Signal Control System (Recall)



SAF 1 There is at most one train on each block

LIVE 2 Each train in the network eventually leaves

Refinement strategy: Prioritise LIVE 2 first.

A Signal Control System. The Initial Model

- Focus on **trains in the network**
- Set *TRN* denotes the set of possible trains.
- Variable *trns* denotes the set of trains in the network.
- Event **arrive** models a train entering the network.
- Event **depart** models a train leaving the network.

```

arrive
  any t where
    t ∈ TRN
  then
    trns := trns ∪ {t}
  end
    
```

```

depart
  any t where
    t ∈ TRN
  during
    t ∈ trns
  then
    trns := trns \ {t}
  end
    
```

Outline

- 1 Formal Systems Development using Refinement
- 2 **The Unit-B Modelling Method**
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Execution and Properties

Execution and Properties

M satisfies ϕ if and only if $\text{ex.M} \Rightarrow \phi$.

Safety Properties

- **Invariance** properties: (in LTL $\square I$)
 - I holds for every reachable state.
 - Proved using the standard **induction technique**.
- **Unless** properties: $P \text{ un } Q$
 - if P holds at some point then it continues to hold unless Q holds.
 - Prove: If for every event

$e \hat{=} \text{any } t \text{ where } G.t.v \text{ during } \dots \text{ upon } \dots \text{ then } S.t.v.v' \text{ end}$

in \mathbf{M} , we have

$$P.v \wedge \neg Q.v \wedge G.t.v \wedge S.t.v.v' \Rightarrow P.v' \vee Q.v' \quad (\text{UN})$$

then \mathbf{M} satisfies $P \text{ un } Q$.

Liveness Properties

- **Progress** properties $P \rightsquigarrow Q$.
- In LTL: $\Box(P \Rightarrow \Diamond Q)$
- Some important rules

$$\begin{array}{lll}
 (P \Rightarrow Q) \Rightarrow (P \rightsquigarrow Q) & & \text{(Implication)} \\
 (P \rightsquigarrow Q) \wedge (Q \rightsquigarrow R) \Rightarrow (P \rightsquigarrow R) & & \text{(Transitivity)} \\
 (P \rightsquigarrow Q) \Leftrightarrow (P \wedge \neg Q \rightsquigarrow Q) & & \text{(Split-Off-Skip)}
 \end{array}$$

A Signal Control System. The Initial Model

Properties

LIVE 2 Each train in the network eventually leaves

properties :

`prg0_1` : $t \in trns \rightsquigarrow t \notin trns$

Note: Free-variables are universally quantified.

Transient Properties (1/3)

Definition

- Borrowed from UNITY.
- The basic tool for reasoning about progress properties.
- $\text{tr } P$ states that always P is eventually falsified.
- In LTL: $\Box \Diamond \neg P$.
- Important properties:

$$\text{tr } P = \top \rightsquigarrow \neg P = P \rightsquigarrow \neg P$$

Transient Properties (2/3)

Theorem (Implementing **tr**)

if there exists an event

$e \hat{=} \text{any } t \text{ where } G.t.v \text{ during } C.t.v \text{ upon } F.t.v \text{ then } S.t.v.v' \text{ end}$

in \mathbf{M} such that

$$\Box(P \Rightarrow C), \quad (\text{SCH})$$

$$C \rightsquigarrow F, \quad (\text{PRG})$$

$$P.v \wedge C.t.v \wedge F.t.v \wedge G.t.v \wedge S.t.v.v' \Rightarrow \neg P.v' \quad (\text{NEG})$$

then \mathbf{M} satisfies **tr** P .

- (SCH) corresponds to an invariance property.
- (PRG) is trivial when F is \top .
- (NEG) corresponds to a standard Hoare-triple.

Transient Properties (3/3)

A Sketch Proof

Consider $\text{tr } P = P \rightsquigarrow \neg P = \Box(P \Rightarrow \Diamond \neg P)$.

Proof (Sketch).

Assume P holds in some state, we prove $\Diamond \neg P$ by contradiction.

- 1 Assume $\Box P$.
- 2 From (SCH), we have $\Box C$,
- 3 together with (PRG), we have $\Box \Diamond F$.
- 4 Scheduling assumption ensures that **e will eventually occur**.
- 5 (NEG) guarantees that when **e** occurs, **P is falsified**.
- 6 We have a **contradiction** with the assumption from Step 1



A Signal Control System. The Initial Model

Properties

```
depart
  any  $t$  where
     $t \in TRN$ 
  during
     $t \in trns$ 
  then
     $trns := trns \setminus \{t\}$ 
  end
```

$prg0_1 : t \in trns \rightsquigarrow t \notin trns$

- $prg0_1$ is the same as $\mathbf{tr} t \in trns$
- (SCH) is trivial.
- No fine-schedule (F is \top) hence (PRG) is trivial.
- The event falsifies $t \in trns$ (NEG)

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Refinement

- Abstract systems can **simulate** behaviours of concrete systems.

$$\text{ex.cncM} \Rightarrow \text{ex.absM}$$

- **Event-based** reasoning.

$(\text{abs_})e \hat{=} \text{any } t \text{ where } G \text{ during } C \text{ upon } F \text{ then } S \text{ end}$

$(\text{cnc_})f \hat{=} \text{any } t \text{ where } H \text{ during } D \text{ upon } E \text{ then } R \text{ end}$

- Safety:

- Guard strengthening: $H \Rightarrow G$
- Action strengthening: $R \Rightarrow S$

- Liveness:

- Liveness assumption strengthening.
- Schedules weakening:

$$(\Box C \wedge \Box \Diamond F) \Rightarrow (\Box D \wedge \Box \Diamond E)$$

Schedules Weakening

Practical Rules

$$(\Box C \wedge \Box \Diamond F) \Rightarrow (\Box D \wedge \Box \Diamond E) \quad (\text{REF_LIVE})$$

- Practical rules:

- Coarse-schedule following

$$C \wedge F \rightsquigarrow D \quad (\text{C_FLW})$$

- Coarse-schedule stabilising

$$D \text{ un } C \quad (\text{C_STB})$$

- Fine-schedule following

$$C \wedge F \rightsquigarrow E \quad (\text{F_FLW})$$

Schedules Weakening

Practical Rules

$$(\Box C \wedge \Box \Diamond F) \Rightarrow (\Box D \wedge \Box \Diamond E) \quad (\text{REF_LIVE})$$

- **Practical** rules:

- Coarse-schedule following

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- Coarse-schedule stabilising

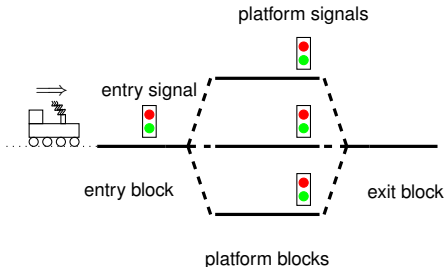
$$D \text{ un } C \quad (\text{C_STB})$$

- Fine-schedule following

$$C \wedge F \rightsquigarrow E \quad (\text{F_FLW})$$

A Signal Control System. The First Refinement

The State



- Introduce the network topology: *BLK*, *Entry*, *PLF*, *Exit*.
- Variable *loc* denotes location of trains in the network.

$$\text{inv1_1} : \text{loc} \in \text{trns} \rightarrow \text{BLK}$$

A Signal Control System. The First Refinement

Refinement of `depart`

```
(abs_)depart
  any t where
    t ∈ TRN
  during
    t ∈ trns
  then
    trns := trns \ {t}
  end
```

```
(cnc_)depart
  any t where
    t ∈ trns ∧ loc.t = Exit
  during
    t ∈ trns ∧ loc.t = Exit
  then
    trns := trns \ {t}
    loc := {t} ◁ loc
  end
```

- Guard and action strengthening are trivial.
- Coarse-schedule following (amongst others):

$$t \in trns \rightsquigarrow t \in trns \wedge loc.t = Exit \quad (\text{prg1_1})$$

A Signal Control System. The First Refinement

Refinement of `depart`

$$t \in trns \rightsquigarrow t \in trns \wedge loc.t = Exit$$

\Leftrightarrow

Put the negation of RHS in the LHS

$$t \in trns \wedge loc.t \neq Exit \rightsquigarrow t \in trns \wedge loc.t = Exit$$

\Leftarrow

Transitivity

$$t \in trns \wedge loc.t \neq Exit \rightsquigarrow t \in trns \wedge loc.t \in PLF \wedge$$

$$t \in trns \wedge loc.t \in PLF \rightsquigarrow t \in trns \wedge loc.t = Exit$$

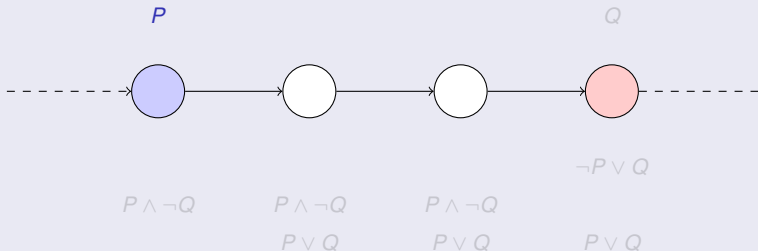
- The 1st condition is implemented by event `movein` (not shown)
- The 2nd condition is implemented by event `moveout`
- We need **the ensure rule** (next slide).

The Ensure Rule

Theorem (The ensure-rule)

For all state predicates p and q ,

$$(P \text{ un } Q) \wedge (\text{tr } P \wedge \neg Q) \Rightarrow (P \rightsquigarrow Q) \quad (\text{ENS})$$

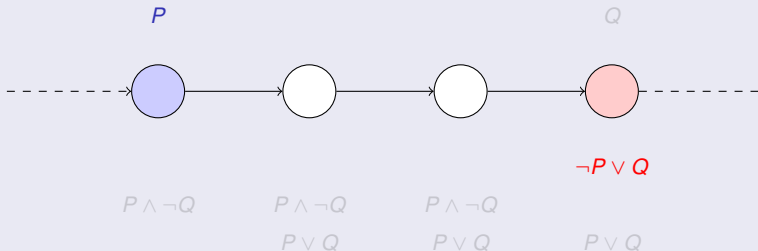


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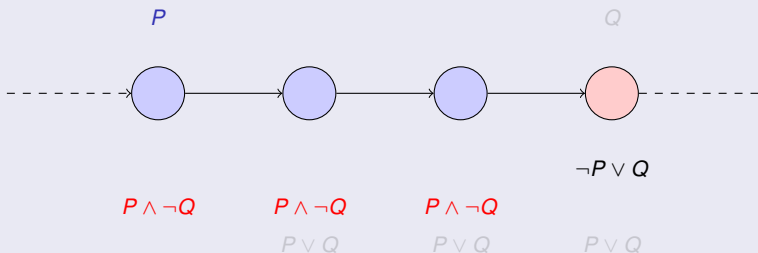


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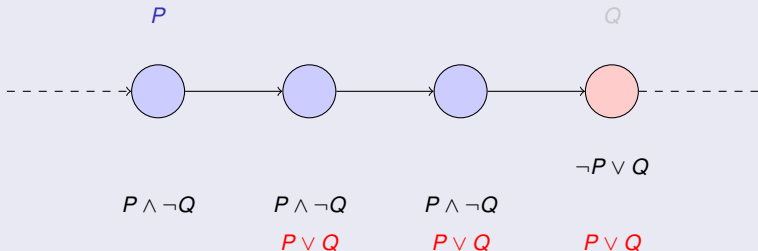


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For all state predicates p and q ,

$$(P \text{ un } Q) \wedge (\text{tr } P \wedge \neg Q) \Rightarrow (P \rightsquigarrow Q) \quad (\text{ENS})$$



A Signal Control System. The First Refinement

New Event `moveout`

$t \in trns \wedge loc.t \in PLF \rightsquigarrow t \in trns \wedge loc.t = Exit$ Ensure rule

\Leftarrow

$t \in trns \wedge loc.t \in PLF$ **un** $t \in trns \wedge loc.t = Exit \wedge$
 $(\mathbf{tr}(t \in trns \wedge loc.t \in PLF) \wedge \neg(t \in trns \wedge loc.t = Exit))$

\Leftrightarrow Logic

$\dots \wedge (\mathbf{tr} t \in trns \wedge loc.t \in PLF)$

```

moveout
  any t where
    t ∈ trns ∧ loc.t ∈ PLF
  during
    t ∈ trns ∧ loc.t ∈ PLF
  then
    loc.t := Exit
  end
    
```


A Signal Control System. The First Refinement

New Event **moveout**

$t \in trns \wedge loc.t \in PLF \rightsquigarrow t \in trns \wedge loc.t = Exit$ Ensure rule

\Leftarrow

$t \in trns \wedge loc.t \in PLF$ **un** $t \in trns \wedge loc.t = Exit \wedge$
 $(\mathbf{tr}(t \in trns \wedge loc.t \in PLF) \wedge \neg(t \in trns \wedge loc.t = Exit))$ Logic

\Leftrightarrow

$\dots \wedge (\mathbf{tr} t \in trns \wedge loc.t \in PLF)$

moveout

any t **where**

$t \in trns \wedge loc.t \in PLF$

during

$t \in trns \wedge loc.t \in PLF$

then

$loc.t := Exit$

end

A Signal Control System. The Second Refinement

The State

SAF 1 There is at most one train on each block

$$\forall t_1, t_2 \cdot t_1 \in trns \wedge t_2 \in trns \wedge loc.t_1 = loc.t_2 \Rightarrow t_1 = t_2$$

A Signal Control System. The Second Refinement

Refinement of *moveout*

```
(abs_)moveout
  any t where
     $t \in trns \wedge loc.t \in PLF$ 
  during
     $t \in trns \wedge loc.t \in PLF$ 
  then
     $loc.t := Exit$ 
  end
```

```
(cnc_)moveout
  any t where
     $t \in trns \wedge loc.t \in PLF \wedge$   

     $Exit \notin ran .loc$ 
  during
     $t \in trns \wedge loc.t \in PLF$ 
  upon
     $Exit \notin ran .loc$ 
  then
     $loc.t := Exit$ 
  end
```

- Neither weak- nor strong-fairness is satisfactory.
 - Weak-fairness requires *Exit* to be free infinitely long.
 - Strong-fairness is too strong assumption.

A Signal Control System. The Second Refinement

Refinement of *moveout*

```
(abs_)moveout
  any t where
    t ∈ trns ∧ loc.t ∈ PLF
  during
    t ∈ trns ∧ loc.t ∈ PLF
  then
    loc.t := Exit
  end
```

```
(cnc_)moveout
  any t where
    t ∈ trns ∧ loc.t ∈ PLF ∧
    Exit ∉ ran .loc
  during
    t ∈ trns ∧ loc.t ∈ PLF
  upon
    Exit ∉ ran .loc
  then
    loc.t := Exit
  end
```

- Neither weak- nor strong-fairness is satisfactory.
 - Weak-fairness requires *Exit* to be free infinitely long.
 - Strong-fairness is too strong assumption.

A Signal Control System. The Second Refinement

Refinement of *moveout*

```
(abs_)moveout
  any t where
     $t \in trns \wedge loc.t \in PLF$ 
  during
     $t \in trns \wedge loc.t \in PLF$ 
  then
     $loc.t := Exit$ 
  end
```

```
(cnc_)moveout
  any t where
     $t \in trns \wedge loc.t \in PLF \wedge$   

     $Exit \notin ran.loc$ 
  during
     $t \in trns \wedge loc.t \in PLF$ 
  upon
     $Exit \notin ran.loc$ 
  then
     $loc.t := Exit$ 
  end
```

- Neither weak- nor strong-fairness is satisfactory.
 - Weak-fairness requires *Exit* to be free infinitely long.
 - Strong-fairness is too strong assumption.

A Signal Control System. The Third Refinement

The State

- Introduce the signals *sgn*

inv3_1 : $sgn \in \{Entry\} \cup PLF \rightarrow COLOR$

inv3_2 : $\forall p. p \in PLF \wedge sgn.p = GR \Rightarrow Exit \notin \text{ran}.loc$

inv3_3 : $\forall p, q. p, q \in PLF \wedge sgn.p = sgn.q = GR \Rightarrow p = q$

A Signal Control System. The Third Refinement

Refinement of *moveout*

```
(abs_)moveout
any t where
  t ∈ trns ∧ loc.t ∈ PLF ∧
  Exit ∉ ran .loc
during
  t ∈ trns ∧ loc.t ∈ PLF
upon
  Exit ∉ ran .loc
then
  loc.t := Exit
end
```

```
(cnc_)moveout
any t where
  t ∈ trns ∧ loc.t ∈ PLF ∧
  sgn.(loc.t) = GR
during
  t ∈ trns ∧ loc.train ∈ PLF ∧
  sgn.(loc.t) = GR
then
  loc.t := Exit
  sgn.(loc.t) := RD
end
```

Refinement requires to prove:

tr *t* ∈ *trns* ∧ *loc.t* ∈ *PLF* ∧ *sgn.(loc.t)* = *RD* . (*prg3_5*)

A Signal Control System. The Third Refinement

New Controller Event `ctrl_platform`

`ctrl_platform`

any p **where**

$p \in PLF \wedge p \in \text{ran}.loc \wedge Exit \notin \text{ran}.loc \wedge$
 $(\forall q \cdot q \in PLF \Rightarrow \text{sgn}.q = RD)$

during

$p \in PLF \wedge p \in \text{ran}.loc \wedge \text{sgn}.p = RD$

upon

$Exit \notin \text{ran}(loc) \wedge (\forall q \cdot q \in PLF \wedge q \neq p \Rightarrow \text{sgn}.q = RD)$

then

$\text{sgn}.p := GR$

end

Summary

The Unit-B Modelling Method

- Guarded and **scheduled** events.
- Reasoning about **liveness (progress) properties**.
- **Refinement** preserving safety and liveness properties.
- Developments are **guided by safety and liveness requirements**.

Summary

Future Work

- Data refinement
- Decomposition / Composition
- Tool support

References I



Simon Hudon.

A Progress Preserving Refinement.

Master Thesis.

Chair of Information Security, ETH Zurich, 2011.



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Systems Design Guided by Progress Concerns.

Accepted for *iFM 2013*.