

Proof Rules for Invariance and Liveness Properties

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Discrete Transition Systems (Recall)

Given the following transition system S

system S
variables $\bar{v} \in \bar{T}$
initially $init(\bar{v})$
events
 $evt_i \triangleq G_i(\bar{v}) \longrightarrow \bar{v} := \bar{f}_i(\bar{v})$

- \bar{v} denotes the vector of **variables** v_1, \dots, v_n .
- $init(\bar{v})$ is the **initialisation**.
- $G_i(\bar{v})$ is the **guard** of event evt_i .
- evt_i is said to be **enabled** in some state s if $G_i(\bar{v})$ holds in s .

- $\bar{v} := \bar{f}_i(\bar{v})$ is the **action** of event evt_i .

Executions and Traces (of States)

Executions $\alpha = s_0 \xrightarrow{a_0} s_1 \xrightarrow{a_1} s_2 \xrightarrow{a_2} s_3 \xrightarrow{a_3} \dots$

Traces $\sigma = s_0, s_1, s_2, s_3, \dots$

$\mathcal{T}(S)$ denotes the **set of all traces** of system S .

Example

system Counter **events**
variables $c \in \mathbb{Z}$ $inc \triangleq c \neq 5 \longrightarrow c := c + 1$
initially $c = 0$ $dec \triangleq c > 3 \longrightarrow c := c - 1$

$\sigma_{Counter} : \langle 0 \rangle, \langle 1 \rangle, \langle 2 \rangle, \langle 3 \rangle, \langle 4 \rangle, \langle 5 \rangle, \langle 4 \rangle, \langle 3 \rangle, \langle 4 \rangle, \langle 5 \rangle, \dots$

Outline

- 1 The Language of Temporal Logic
- 2 Proof Principles
- 3 Example. Reader and Writer
- 4 Conclusions

Temporal Formulas

Temporal formulas to be interpreted over traces.

- A (basic) state formula $Q(\bar{v})$ is any first-order logic formula, e.g. $0 \leq c, \neg(c = 0) \wedge c < 2, \forall m \cdot m \neq 0 \Rightarrow m \leq c$.
- The basic formulas can be extended by combining the Boolean operators ($\neg, \wedge, \vee, \Rightarrow$) with temporal operators:
 - \square : always
 - \diamond : eventually
 - \mathcal{U} : until
- Example of extended formulas:
 - $\square c \in 0..5$
 - $\square c \leq 6$
 - $\diamond c = 2$
 - $\square \diamond c = 2$
 - $c \leq 2 \mathcal{U} c = 2$
 - $\square(c \leq 2 \mathcal{U} c = 2)$
 - $(\square c \in 0..5) \wedge (\diamond c = 2)$

Length and Suffixes of Traces

Let $\sigma : s_0, s_1, \dots$ be any non-empty trace.

- The length of σ denoted by $l(\sigma)$.
 - Finite trace $\sigma : s_0, \dots, s_k: l(\sigma) = k + 1$.
 - Infinite trace: $l(\sigma) = \infty$.
- For $0 \leq k < l(\sigma)$, k -suffix of σ is defined as

$$\sigma^{(k)} = s_k, s_{k+1}, \dots$$

Interpretation

$\sigma \models \phi$ means that a trace σ satisfies formula ϕ

- For state formula ϕ , $\sigma \models \phi$ if and only if s_0 satisfies ϕ .
- Boolean operators are interpreted in the natural way, e.g.
 - $\sigma \models \phi_1 \wedge \phi_2$ if and only if $\sigma \models \phi_1$ and $\sigma \models \phi_2$.
- Temporal operators are interpreted as follows.

$\sigma \models \square \phi$ if and only if $\forall k \cdot 0 \leq k < l(\sigma), \sigma^{(k)} \models \phi$

$\sigma \models \diamond \phi$ if and only if $\exists k \cdot 0 \leq k < l(\sigma), \sigma^{(k)} \models \phi$

$\sigma \models \phi_1 \mathcal{U} \phi_2$ if and only if $\exists k \cdot 0 \leq k < l(\sigma)$ such that $\sigma^{(k)} \models \phi_2$ and $\forall i \cdot 0 \leq i < k, \sigma^{(i)} \models \phi_1$

Interpretation Intuition

For the simple cases, when ϕ, ϕ_1, ϕ_2 are state predicates.

σ satisfies $\square \phi$ if and only if all states in σ satisfy ϕ

σ satisfies $\diamond \phi$ if and only if some states in σ satisfy ϕ

σ satisfies $\phi_1 \mathcal{U} \phi_2$ if and only if some state s_k satisfies ϕ_2 and all the states until s_k (excluding s_k) satisfy ϕ_1

$\sigma_{Counter} : \langle 0 \rangle, \langle 1 \rangle, \langle 2 \rangle, \langle 3 \rangle, \langle 4 \rangle, \langle 5 \rangle, \langle 4 \rangle, \langle 3 \rangle, \langle 4 \rangle, \langle 5 \rangle, \dots$

- $\sigma_{Counter} \models \square c \in 0..5$
- $\sigma_{Counter} \models \square c \leq 6$
- $\sigma_{Counter} \models \diamond c = 2$
- $\sigma_{Counter} \not\models \square \diamond c = 2$
- $\sigma_{Counter} \models c \leq 2 \mathcal{U} c = 2$
- $\sigma_{Counter} \not\models \square(c \leq 2 \mathcal{U} c = 2)$
- $\sigma_{Counter} \models (\square c \in 0..5) \wedge (\diamond c = 2)$

Safety v.s. Liveness

- Safety properties: *something (bad) will never happen.*
 - Example: invariance properties.
 - Typically expressed by a temporal formula: $\Box \phi$ or $\phi_1 \Rightarrow \Box \phi_2$.
- Liveness properties: *something (good) will happen.*
 - Example: termination, responsiveness.
 - Typically expressed by a temporal formula: $\Diamond \phi$ or $\Box(\phi_1 \Rightarrow \Diamond \phi_2)$.
 - Extended: $\phi_1 \mathcal{U} \phi_2$ or $\Box(\phi_1 \Rightarrow \phi_2 \mathcal{U} \phi_3)$.

System Properties

- A system S satisfying property ϕ if **all its traces satisfy ϕ .**
 $S \models \phi$ if and only if $\sigma \models \phi$, for all $\sigma \in \mathcal{T}(S)$.
- $S \vdash \phi$ states that $S \models \phi$ is **provable.**

Proof Tools (1 of 3)

System Leads from ϕ_1 to ϕ_2

Event leads from ϕ_1 to ϕ_2

- Let evt be an event of the form $G(\bar{v}) \rightarrow \bar{v} := \bar{f}(\bar{v})$
- Let $\phi_1(\bar{v})$ and $\phi_2(\bar{v})$ be two state formulas.
- Event evt leads from $\phi_1(\bar{v})$ to $\phi_2(\bar{v})$ if

$$\phi_1(\bar{v}) \wedge G(\bar{v}) \Rightarrow \phi_2(\bar{f}(\bar{v}))$$

System leads from ϕ_1 to ϕ_2

A system S leads from ϕ_1 to ϕ_2 if every event evt in S leads from ϕ_1 to ϕ_2

- When S leads from ϕ_1 to ϕ_2 is **provable**, we write

$$\vdash S \text{ leads from } \phi_1 \text{ to } \phi_2$$

Invariance Rules (1/2)

$$\frac{\begin{array}{l} \vdash \text{init}(\bar{v}) \Rightarrow \phi \\ \vdash S \text{ leads from } \phi \text{ to } \phi \end{array}}{S \vdash \Box \phi} \text{ INV}_{\text{induct}}$$

Counter $\vdash \Box c \in 0..5$

system Counter	events
variables $c \in \mathbb{Z}$	inc $\hat{=}$ $c \neq 5 \rightarrow c := c + 1$
initially $c = 0$	dec $\hat{=}$ $c > 3 \rightarrow c := c - 1$

- Initialisation: $\vdash c = 0 \Rightarrow c \in 0..5$
- inc: $c \in 0..5 \wedge c \neq 5 \Rightarrow c + 1 \in 0..5$
- dec: $c \in 0..5 \wedge c > 3 \Rightarrow c - 1 \in 0..5$

Invariance Rules (2/2)

$$\frac{\begin{array}{l} \vdash \phi_2 \Rightarrow \phi_1 \\ S \vdash \Box \phi_2 \end{array}}{S \vdash \Box \phi_1} \text{ INV}_{\text{theorem}}$$

Counter $\vdash \Box c \leq 6$

Choose ϕ_2 to be $c \in 0 \dots 5$.

- $\vdash c \in 0 \dots 5 \Rightarrow c \leq 6$
- Counter** $\vdash \Box c \in 0 \dots 5$

Proof Tools (2 of 3)

Convergence

- Let ϕ be a state formula.
- A trace is said to be **convergent** when ϕ holds if it **does not end with an infinite sequences of states** satisfying ϕ .
- System S is said to be **convergent** when ϕ holds if **all its traces are convergent** when ϕ holds.
- When the above fact is **provable**, we denote it as

$$\vdash S \downarrow \phi$$

Technique

- For system S with events $\text{evt}_i \triangleq G_i(\bar{v}) \rightarrow \bar{v} := \bar{f}_i(\bar{v})$
- Give a **integer variant** $V(\bar{v})$
- S converges when ϕ holds if for all events evt_i of S
 - $\phi(\bar{v}) \wedge G_i(\bar{v}) \Rightarrow V(\bar{v}) \in \mathbb{N}$
 - $\phi(\bar{v}) \wedge G_i(\bar{v}) \Rightarrow V(\bar{f}_i(\bar{v})) < V(\bar{v})$

Proof Tools (3 of 3)

Deadlock-freeness

- Let ϕ be a state formula.
 - System S is **deadlock-free when ϕ holds** if there exists an **enabled event** of S when ϕ holds.
 - When the above fact is **provable**, we denote it as
- $$\vdash S \text{ is deadlock-free when } \phi \text{ holds}$$
- This is guaranteed by proving the following.

$$\phi(\bar{v}) \Rightarrow G_1(\bar{v}) \vee \dots \vee G_n(\bar{v})$$

Liveness Rules (1/3)

Always Eventually

$$\frac{\begin{array}{l} \vdash S \downarrow \neg \phi \\ \vdash S \text{ is deadlock-free when } \neg \phi \text{ holds} \end{array}}{S \vdash \Box \diamond \phi} \text{ LIVE}_{\Box \diamond}$$

Counter $\vdash \Box \diamond c \geq 2$

- Convergence: Using variant $V \triangleq 5 - c$.
 - $5 - c \in \mathbb{N}$ (using invariant $c \in 0 \dots 5$)
 - inc:** $\neg c \geq 2 \wedge c \neq 5 \Rightarrow 5 - (c + 1) < 5 - c$
 - dec:** $\neg c \geq 2 \wedge c > 3 \Rightarrow 5 - (c - 1) < 5 - c$
- Deadlock-free: $\neg c \geq 2 \Rightarrow c \neq 5 \vee c > 3$

Liveness Rules (2/3)

Until

$$\frac{\begin{array}{l} \vdash S \text{ leads from } \phi_1 \wedge \neg\phi_2 \text{ to } \phi_1 \vee \phi_2 \\ S \vdash \Box\Diamond(\neg\phi_1 \vee \phi_2) \end{array}}{S \vdash \Box(\phi_1 \Rightarrow \phi_1 \mathcal{U} \phi_2)} \text{ LIVE}_{\mathcal{U}}$$

Counter $\vdash \Box(c < 2 \Rightarrow (c < 2 \mathcal{U} c = 2))$

- Counter leads from $c < 2 \wedge \neg c = 2$ to $c < 2 \vee c = 2$, equivalently Counter leads from $c < 2$ to $c \leq 2$
 - inc: $c < 2 \wedge c \neq 5 \Rightarrow c + 1 \leq 2$
 - dec: $c < 2 \wedge c > 3 \Rightarrow c - 1 \leq 2$
- Eventually: $\Box\Diamond(\neg c < 2 \vee c = 2)$, equivalent to $\Box\Diamond c \geq 2$

Liveness Rules (3/3)

Response

$$\frac{\begin{array}{l} S \vdash \Box(\phi_1 \Rightarrow \phi_3) \\ S \vdash \Box(\phi_3 \Rightarrow (\phi_3 \mathcal{U} \phi_2)) \end{array}}{S \vdash \Box(\phi_1 \Rightarrow \Diamond\phi_2)} \text{ LIVE}_{\text{response}}$$

Counter $\vdash \Box(c = 0 \Rightarrow \Diamond c = 2)$

Choose $\phi_3 \hat{=} c < 2$

- $\Box(c = 0 \Rightarrow c < 2)$
- $\Box(c < 2 \Rightarrow (c < 2 \mathcal{U} c = 2))$

Example. Reader and Writer

system <i>RdWr</i>	events
variables $r, w \in \mathbb{Z}, \mathbb{Z}$	read $\hat{=} r \neq w \rightarrow r := r + 1$
initially $r = 0 \wedge w = 0$	write $\hat{=} w < r + 3 \rightarrow w := w + 1$

An execution

$\langle 0, 0 \rangle \xrightarrow{\text{write}} \langle 0, 1 \rangle \xrightarrow{\text{write}} \langle 0, 2 \rangle \xrightarrow{\text{write}} \langle 0, 3 \rangle \xrightarrow{\text{read}} \langle 1, 3 \rangle \xrightarrow{\text{read}} \langle 2, 3 \rangle \xrightarrow{\text{read}} \langle 3, 3 \rangle$
 $\xrightarrow{\text{write}} \langle 3, 4 \rangle \xrightarrow{\text{write}} \langle 3, 5 \rangle \xrightarrow{\text{read}} \langle 4, 5 \rangle \xrightarrow{\text{write}} \langle 4, 6 \rangle \xrightarrow{\text{read}} \langle 5, 6 \rangle \xrightarrow{\text{write}} \langle 5, 7 \rangle \dots$

A Progress Properties

Reader's progress

The Reader will eventually read the data that the Writer wrote.

Formalisation. First attempt

- Can we prove $RdWr \models \Box\Diamond r = w$?
 - No, the Reader might always be behind the Writer (despite progressing).

Formalisation. Second attempt

$RdWr \models \Box(w = K \Rightarrow \Diamond r = K)$?

A Proof (1/6)

system $RdWr$ events
 variables $r, w \in \mathbb{Z}, \mathbb{Z}$ read $\hat{=}$ $r \neq w \rightarrow r := r + 1$
 initially $r = 0 \wedge w = 0$ write $\hat{=}$ $w < r + 3 \rightarrow w := w + 1$

$$\begin{array}{l} \text{LIVE}_{\text{response}} \rightarrow \left\{ \begin{array}{l} \boxed{RdWr \vdash \square(w = K \Rightarrow \diamond r = K)} \\ \boxed{RdWr \vdash \square(w = K \Rightarrow \phi_3)} \quad (1) \\ \boxed{RdWr \vdash \square(\phi_3 \Rightarrow (\phi_3 \cup r = K))} \quad (2) \end{array} \right. \\ \\ \frac{S \vdash \square(\phi_1 \Rightarrow \phi_3) \quad S \vdash \square(\phi_3 \Rightarrow (\phi_3 \cup \phi_2))}{S \vdash \square(\phi_1 \Rightarrow \diamond \phi_2)} \text{LIVE}_{\text{response}} \end{array}$$

A Proof (2/6)

system $RdWr$ events
 variables $r, w \in \mathbb{Z}, \mathbb{Z}$ read $\hat{=}$ $r \neq w \rightarrow r := r + 1$
 initially $r = 0 \wedge w = 0$ write $\hat{=}$ $w < r + 3 \rightarrow w := w + 1$

$$\begin{array}{l} \frac{\vdash \bar{v} = \bar{s}_0 \Rightarrow \phi \quad \vdash S \text{ leads from } \phi \text{ to } \phi}{S \vdash \square \phi} \text{INV}_{\text{induct}} \quad \frac{\vdash \phi_2 \Rightarrow \phi_1 \quad S \vdash \square \phi_2}{S \vdash \square \phi_1} \text{INV}_{\text{theorem}} \\ \\ (1) \quad \boxed{RdWr \vdash \square(w = K \Rightarrow r \leq K)} \\ \\ \text{INV}_{\text{theorem}} \rightarrow \left\{ \begin{array}{l} \boxed{r \leq w \Rightarrow (w = K \Rightarrow r \leq K)} \\ \boxed{RdWr \vdash \square r \leq w} \end{array} \right. \end{array}$$

• $\text{INV}_{\text{induct}}$ fails, hence apply $\text{INV}_{\text{theorem}}$ with ϕ_2 to be $r \leq w$.

A Proof (3/6)

$$\frac{\vdash S \text{ leads from } \phi_1 \wedge \neg \phi_2 \text{ to } \phi_1 \vee \phi_2 \quad S \vdash \square \diamond (\neg \phi_1 \vee \phi_2)}{S \vdash \square (\phi_1 \Rightarrow \phi_1 \cup \phi_2)} \text{LIVE}_{\cup}$$

$$\begin{array}{l} (2) \quad \boxed{RdWr \vdash \square(r \leq K \Rightarrow (r \leq K \cup r = K))} \\ \\ \text{LIVE}_{\cup} \rightarrow \left\{ \begin{array}{l} \vdash RdWr \text{ leads from } r \leq K \wedge \neg r = K \text{ to } r \leq K \vee r = K \quad (2.1) \\ \boxed{RdWr \vdash \square \diamond (\neg r \leq K \vee r = K)} \quad (2.2) \end{array} \right. \end{array}$$

A Proof (4/6)

system $RdWr$ events
 variables $r, w \in \mathbb{Z}, \mathbb{Z}$ read $\hat{=}$ $r \neq w \rightarrow r := r + 1$
 initially $r = 0 \wedge w = 0$ write $\hat{=}$ $w < r + 3 \rightarrow w := w + 1$

$$\begin{array}{l} (2.1) \quad \boxed{\vdash RdWr \text{ leads from } r \leq K \wedge \neg r = K \text{ to } r \leq K \vee r = K} \\ \\ \text{logic} \rightarrow \boxed{\vdash RdWr \text{ leads from } r < K \text{ to } r \leq K} \\ \\ \text{definition} \rightarrow \left\{ \begin{array}{l} \boxed{r < K \wedge r \neq w \Rightarrow r + 1 \leq K} \\ \boxed{r < K \wedge w < r + 1 \Rightarrow r \leq K} \end{array} \right. \end{array}$$

A Proof (5/6)

$$\frac{\begin{array}{l} \vdash S \downarrow \neg\phi \\ \vdash S \text{ is deadlock-free when } \neg\phi \text{ holds} \end{array}}{S \vdash \square\Diamond\phi} \text{ LIVE}_{\square\Diamond}$$

$$(2.2) \quad \boxed{RdWr \vdash \square\Diamond(\neg r \leq K \vee r = K)}$$

$$\xrightarrow{\text{logic}} \boxed{RdWr \vdash \square\Diamond r \geq K}$$

$$\xrightarrow{\text{LIVE}_{\square\Diamond}} \left\{ \begin{array}{l} \boxed{\vdash RdWr \downarrow \neg r \geq K} \quad (2.2.1) \\ \boxed{\vdash RdWr \text{ is deadlock-free when } \neg r \geq K \text{ holds}} \quad (2.2.2) \end{array} \right.$$

A Proof (6/6)

system $RdWr$	events
variables $r, w \in \mathbb{Z}, \mathbb{Z}$	read $\hat{=}$ $r \neq w \rightarrow r := r + 1$
initially $r = 0 \wedge w = 0$	write $\hat{=}$ $w < r + 3 \rightarrow w := w + 1$

$$(2.2.1) \quad \boxed{\vdash RdWr \downarrow \neg r \geq K}$$

$$\xrightarrow{\text{logic}} \boxed{\vdash RdWr \downarrow r < K}$$

- Use variant:

$$(K - r) \times 2 + (r + 3 - w)$$

$$(2.2.2) \quad \boxed{\vdash RdWr \text{ is deadlock-free when } \neg r \geq K \text{ holds}}$$

$$\xrightarrow{\text{definition}} \boxed{\neg r \geq K \Rightarrow r \neq w \vee w < 3 + r}$$



Summary

- Proof rules for certain classes of **invariance** and **liveness** properties.
- The proof rules based on the reasoning about:
 - the system **leads from** ϕ_1 to ϕ_2
 - the system is **convergence** when ϕ holds
 - the system is **deadlock-free** when ϕ holds.

Further Directions

- Proofs become **tedious** when the system becomes large.
- Refinement** helps to reduce the complexity.
 - Invariance** properties are maintained.
 - How about **liveness**?
- Concurrent systems: **fairness** assumptions.
 - Expect some **weaker rules**.
 - Interaction with **refinement**?

For Further Reading I

-  Zohar Manna and Amir Pnueli.
Adequate Proof Principles
for Invariance and Liveness Properties of Concurrent Programs.
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