# Reasoning about Liveness Properties in Event-B

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ETH

# Event-B Models - Discrete Transition Systems

machine M variables v invariants *I(v)* initialisation K(c, v')events  $\operatorname{evt}_{i} = \operatorname{any} t_{i} \operatorname{where} G_{i}(t_{i}, v) \operatorname{then} S(t_{i}, v, v') \operatorname{end}$ 

- v denotes the vector of variables  $v_1, \ldots, v_n$ .
- K(c, v') is the initialisation.
- *t<sub>i</sub>* is the parameters of event evt<sub>i</sub>.
- $G_i(t_i, v)$  is the guard of event evt<sub>i</sub>.
- evt<sub>i</sub> is said to be enabled in some state s if  $\exists t_i \cdot G_i(t_i, v)$  holds in s.
- $S(t_i, v, v')$  is the action (before-after predicate) of event evt<sub>i</sub>.

### Motivation

- Event-B Models
  - Discrete transition systems
- Safety properties
  - Something (bad) never happens.
  - e.g. invariance properties
  - part of Event-B models
- Liveness properties
  - Something (good) will happen
  - e.g. termination, eventually, progress, persistence
  - How to reason about them practically?

# **Executions and Traces (of States)**

Executions  $\alpha = s_0 \xrightarrow{e_0} s_1 \xrightarrow{e_1} s_2 \xrightarrow{e_2} s_3 \xrightarrow{e_3} \dots$ 

Traces

 $\sigma = s_0, s_1, s_2, s_3, \dots$ 

- Initialisation:  $s_0 = \langle v' \rangle$  (as defined by init)
- Sequencing: For all  $s_k$ ,  $s_{k+1}$ , there exists evt<sub>i</sub> s.t.  $s_k \xrightarrow{evt_i} s_{k+1}$
- Maximality: The sequence is either infinite or ends in a state  $s_k$  where all events are disabled

#### Example

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machine Counter events

variables  $c \in \mathbb{Z}$ 

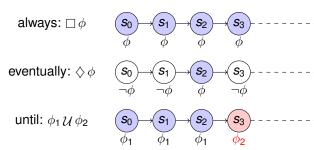
inc  $\widehat{=}$  when  $c \neq 5$  then c := c + 1 end

initialisation c := 0dec  $\widehat{=}$  when c > 3 then c := c - 1 end

e.g.  $\sigma_{Counter}$ :  $\langle 0 \rangle$ ,  $\langle 1 \rangle$ ,  $\langle 2 \rangle$ ,  $\langle 3 \rangle$ ,  $\langle 4 \rangle$ ,  $\langle 5 \rangle$ ,  $\langle 4 \rangle$ ,  $\langle 3 \rangle$ ,  $\langle 4 \rangle$ ,  $\langle 5 \rangle$ , . . .

### The Language of Temporal Logic

- A (basic) state formula P is any first-order logic formula,
- The basic formulas can be extended by combining the Boolean operators (¬, ∧, ∨, ⇒) with temporal operators:



- A machine M satisfying property  $\phi$  if all its traces satisfy  $\phi$ .
- $M \vdash \phi$  states that  $M \models \phi$  is provable.

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# Proof Obligations (1/4)

#### Machine leads from $P_1$ to $P_2$

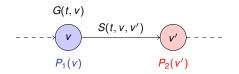
A machine M leads from  $P_1$  to  $P_2$  if every event evt in M leads from  $P_1$  to  $P_2$ 

When M leads from  $P_1$  to  $P_2$  is provable, we write

 $\vdash$  M leads from  $P_1$  to  $P_2$ 

- Given M with  $\text{evt}_i \triangleq \text{any } t_i \text{ where } G_i(t_i, v) \text{ then } S_i(t_i, v, v') \text{ end}$
- Event evt<sub>i</sub> leads from P<sub>1</sub> to P<sub>2</sub> if

$$P_1(v) \wedge G_i(t_i, v) \wedge S_i(t_i, v, v') \Rightarrow P_2(v')$$



### Contribution

Proof rules for some class of liveness properties

■ Eventually: □ ♦ P

• Until:  $\Box(P_1 \Rightarrow (P_1 \cup P_2))$ 

• Progress:  $\Box(P_1 \Rightarrow \Diamond P_2)$ 

Persistence: ♦ □ P

# Proof Obligations (2/4)

#### Machine is convergent in P

A macine M is said to be convergent in *P* if for any trace of M, it does not end with an infinite sequence of states satisfying *P* 

⊢ M is convergent in *P* 

- Given M with evt<sub>i</sub>  $\hat{=}$  any  $t_i$  where  $G_i(t_i, v)$  then  $S_i(t_i, v, v')$  end
- Give a integer variant V(v)
- M converges in P if for all events evt<sub>i</sub> of M, we have

 $P(v) \wedge G_i(t_i, v) \Rightarrow V(v) \in \mathbb{N}$ 

## Proof Obligations (3/4)

#### Machine is deadlock-free in P

- Machine M is deadlock-free in P if there exists an enabled event of M when P holds.
- When the above fact is provable, we denote it as

⊢ M is deadlock-free in P

Deadlock-freeness in P is guaranteed by proving the following

$$P(v) \Rightarrow (\exists t_1 \cdot G_1(t_1, v)) \vee \ldots \vee (\exists t_n \cdot G_n(t_n, v))$$

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### Proof Rules (1/4)

**Always Eventually** 

 $\vdash$  M is convergent in  $\neg P$  $\vdash$  M is deadlock-free in  $\neg P$ 

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 $M \vdash \Box \Diamond P$ 

#### Counter $\vdash \Box \diamondsuit c \ge 2$

inc  $\hat{=}$  when  $c \neq 5$  then c := c + 1 end

 $\operatorname{dec} \ \widehat{=} \ \operatorname{when} \ c > 3 \operatorname{then} \ c := c - 1 \operatorname{end}$ 

- Convergence: Using variant V = 5 c.
  - $5 c \in \mathbb{N}$  (using invariant  $c \in 0...5$ )
  - inc:  $\neg c > 2 \land c \neq 5 \Rightarrow 5 (c+1) < 5 c$
  - dec:  $\neg c \ge 2 \land c > 3 \implies 5 (c 1) < 5 c$
- Deadlock-free:  $\neg c \ge 2 \Rightarrow c \ne 5 \lor c > 3$

### Proof Obligations (4/4)

#### Machine is divergent in P

- M is said to be divergent in P if for every infinite trace of M it ends with an infinite sequences of states satisfying P.
- When the above fact is provable, we denote it as

⊢ M is divergent in *P* 

- Given M with evt<sub>i</sub>  $\hat{=}$  any  $t_i$  where  $G_i(t_i, v)$  then  $S_i(t_i, v, v')$  end
- Give a integer variant V(v)
- M diverges when P holds if for all events evt<sub>i</sub> of M

$$\neg P(v) \land G_i(t_i, v) \Rightarrow V(v) \in \mathbb{N}$$

$$\neg P(v) \land G_i(t_i, v) \land S_i(t_i, v, v') \Rightarrow V(v') < V(v)$$

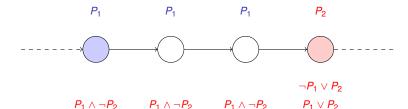
$$P(v) \land G_i(t_i, v) \land S_i(t_i, v, v') \land V(v') \in \mathbb{N} \Rightarrow V(v') \leq V(v)$$

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# Proof Rules (2/4)

Until (1/2)

$$\frac{ \begin{array}{c} \vdash \mathsf{M} \text{ leads from } (P_1 \land \neg P_2) \text{ to } (P_1 \lor P_2) \\ \hline \mathsf{M} \vdash \Box \diamondsuit (\neg P_1 \lor P_2) \end{array}}{\mathsf{M} \vdash \Box (P_1 \Rightarrow (P_1 \, \mathcal{U} \, P_2))} \quad \textbf{Until}$$



### Proof Rules (2/4) Until (2/2)

 $\vdash$  M leads from  $(P_1 \land \neg P_2)$  to  $(P_1 \lor P_2)$  $M \vdash \Box \diamondsuit (\neg P_1 \lor P_2)$ Until  $M \vdash \Box(P_1 \Rightarrow (P_1 \cup P_2))$ 

#### Counter $\vdash \Box (c < 2 \Rightarrow (c < 2 \ \mathcal{U} \ c = 2))$

inc  $\hat{=}$  when  $c \neq 5$  then c := c + 1 end  $\operatorname{dec} \ \widehat{=} \ \operatorname{when} \ c > 3 \operatorname{then} \ c := c - 1 \operatorname{end}$ 

- *Counter* leads from  $c < 2 \land \neg c = 2$  to  $c < 2 \lor c = 2$ , equivalently *Counter* leads from c < 2 to c < 2
  - inc:  $c < 2 \land c \neq 5 \Rightarrow c+1 < 2$
  - dec:  $c < 2 \land c > 3 \implies c 1 \le 2$
- Eventually:  $\Box \lozenge (\neg c < 2 \lor c = 2)$ , equivalent to  $\Box \lozenge c \ge 2$

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### Proof Rules (3/4)

Progress (2/2)

$$\frac{\begin{array}{c} \mathsf{M} \vdash \Box(P_1 \land \neg P_2 \Rightarrow P_3) \\ \mathsf{M} \vdash \Box(P_3 \Rightarrow (P_3 \ \mathit{U} \ P_2)) \\ \hline \\ \mathsf{M} \vdash \Box(P_1 \Rightarrow \diamondsuit \ P_2) \end{array} \quad \mathsf{LIVE}_{\mathsf{progress}}$$

#### Counter $\vdash \Box (c = 0 \Rightarrow \Diamond c = 2)$

inc  $\hat{=}$  when  $c \neq 5$  then c := c + 1 end  $\operatorname{dec} = \operatorname{when} c > 3 \operatorname{then} c := c - 1 \operatorname{end}$ 

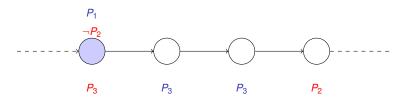
Choose  $P_3 = c < 2$ 

- $\bullet \Box (c < 2 \Rightarrow (c < 2 \mathcal{U} c = 2))$

### Proof Rules (3/4)

Progress (1/2)

$$\frac{ \begin{array}{c} \mathsf{M} \vdash \Box (P_1 \land \neg P_2 \Rightarrow P_3) \\ \mathsf{M} \vdash \Box (P_3 \Rightarrow (P_3 \, \mathcal{U} \, P_2)) \end{array}}{\mathsf{M} \vdash \Box (P_1 \Rightarrow \diamondsuit \, P_2)} \quad \mathsf{LIVE}_{\mathsf{progress}}$$



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## Proof Rules (4/4)

Persistence

⊢ M is divergent in *P*  $\vdash$  M is deadlock-free in  $\neg P$ LIVE⇔□  $M \vdash \Diamond \Box P$ 

#### Counter $\vdash \Diamond \Box c \geq 3$

- Divergence: Using variant V = 2 c
  - $\neg c \geq 3 \Rightarrow 2 c \in \mathbb{N}$
  - inc:  $\neg c > 3 \land c \neq 5 \Rightarrow 2 (c+1) < 2 c$
  - dec:  $\neg c \ge 3 \land c > 3 \Rightarrow 2 (c 1) < 2 c$
  - inc:  $c \ge 3 \land c \ne 5 \land 2 (c+1) \in \mathbb{N} \Rightarrow 2 (c+1) \le 2 c$
  - dec:  $c \ge 3 \land c > 3 \land 2 (c 1) \in \mathbb{N} \Rightarrow 2 (c 1) \le 2 c$
- Deadlock-free:  $\neg c \ge 3 \Rightarrow c \ne 5 \lor c > 3$

### Summary

- Proof rules for certain classes of liveness properties.
  - eventually
  - until
  - progress
  - persistence
- The proof rules based on the reasoning about:
  - the machine leads from  $P_1$  to  $P_2$
  - the machine is convergent when P holds
  - the machine is deadlock-free when P holds.
  - the machine is divergent when P holds

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Appendix

For Further Reading

# For Further Reading I





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# **Further Directions**

- Proofs become tedious when the system becomes large.
- Refinement helps to reduce the complexity.
- Concurrent systems: fairness assumptions.

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