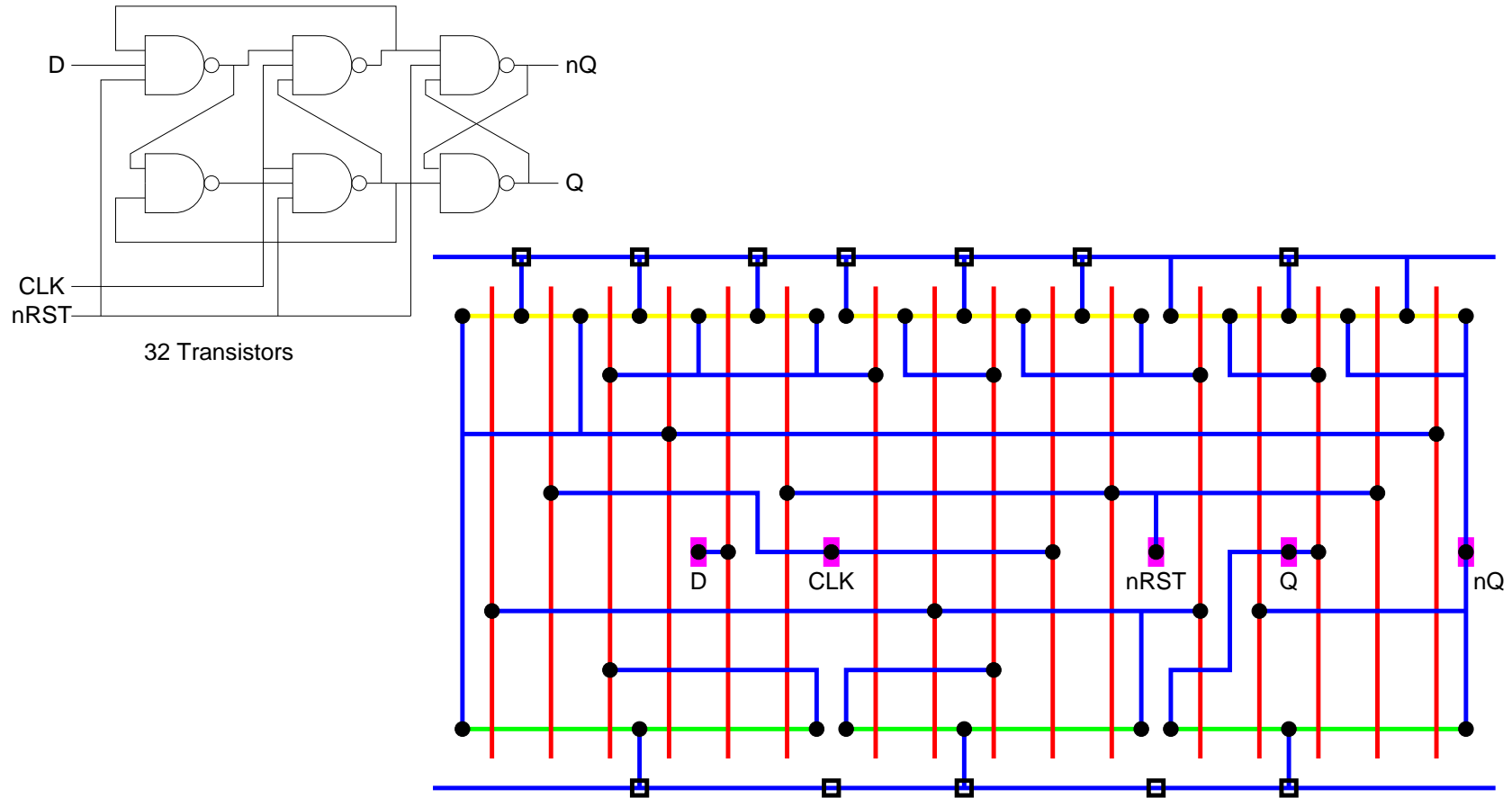
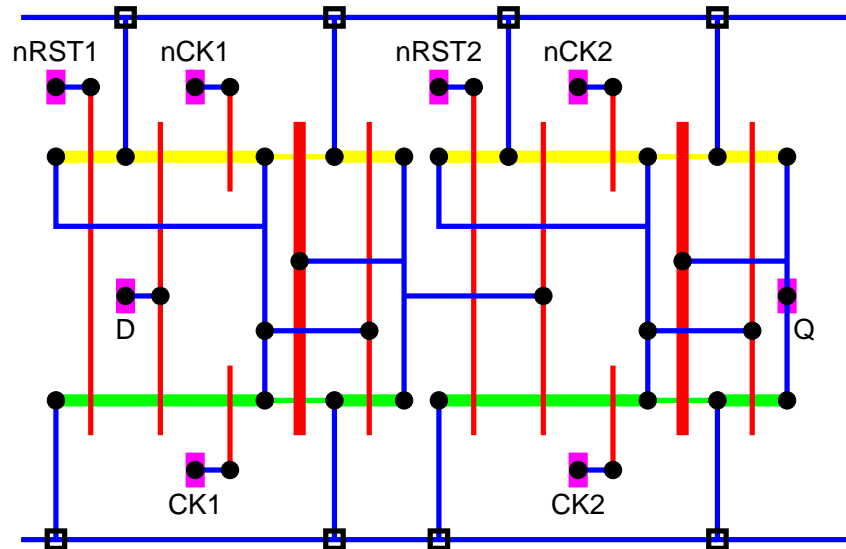
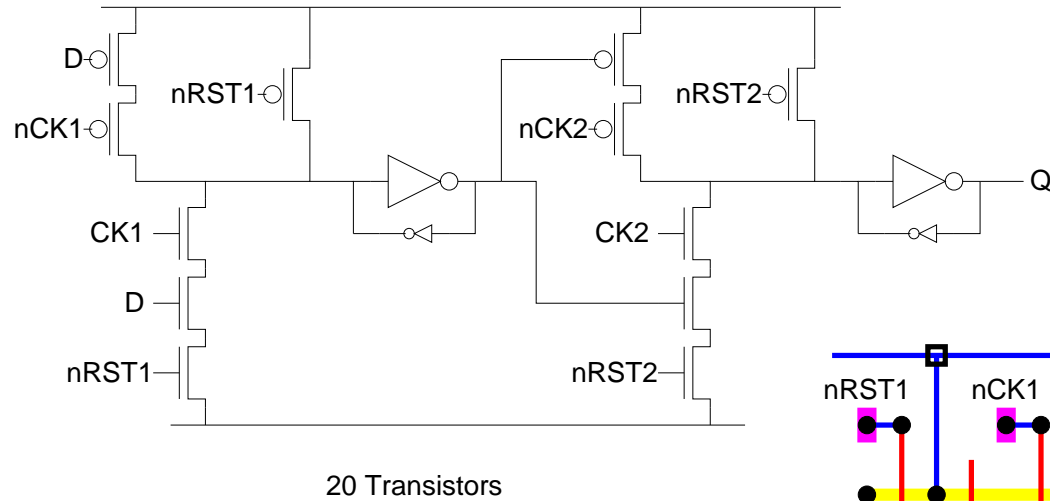


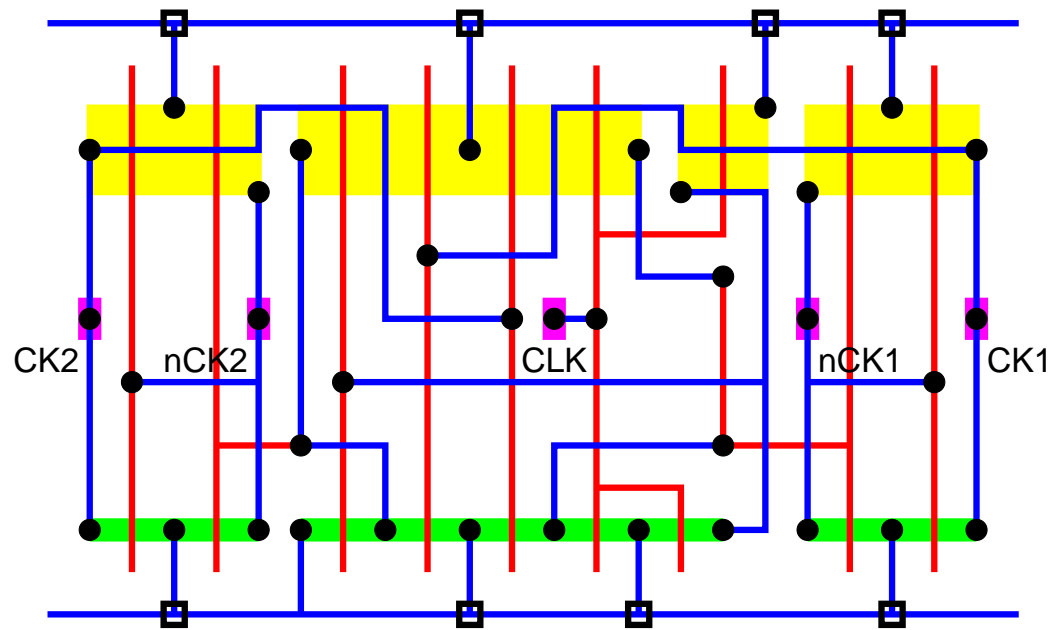
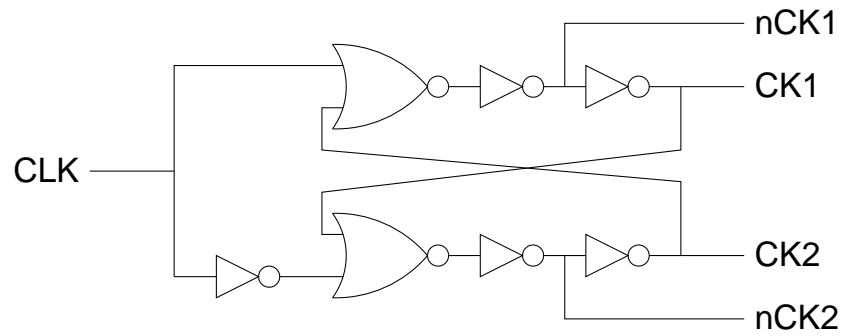
Edge Triggered D-Type with Reset (DFFR)



Master Slave D-Type with Reset (MSDR)



Two Phase Clock Generator (CLKGEN)



Non-Inverting Buffer (BUF)

