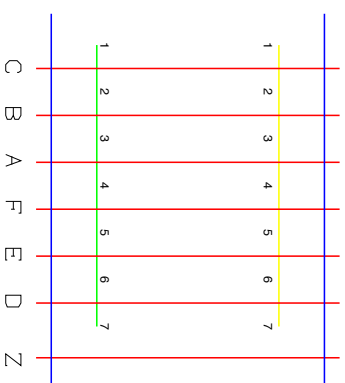
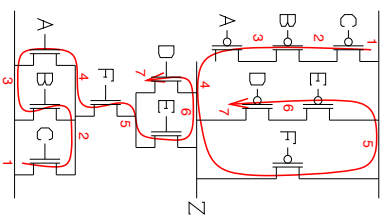


Digital CMOS Design

Finding an Euler Path

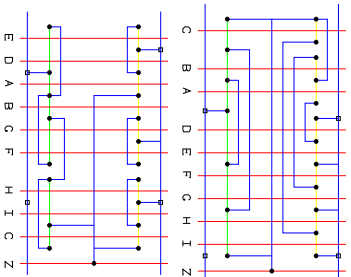
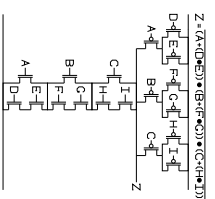
$$Z = \overline{(A+B+C)} \cdot (D+E) \cdot \overline{F}$$



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Digital CMOS Design

Finding an Euler Path



No possible path through p-transistors.
No re-arrangement will create a solution!

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