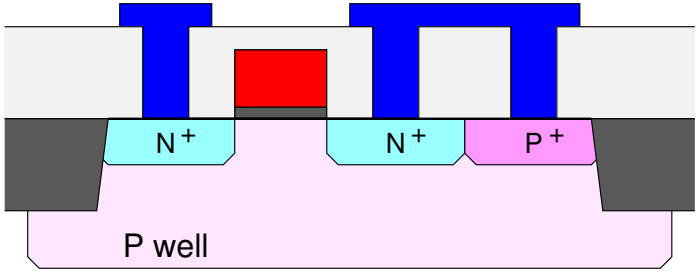
ecs.gg/iquiz

Question 0



Q0. This is a valid cross-section of:[1]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

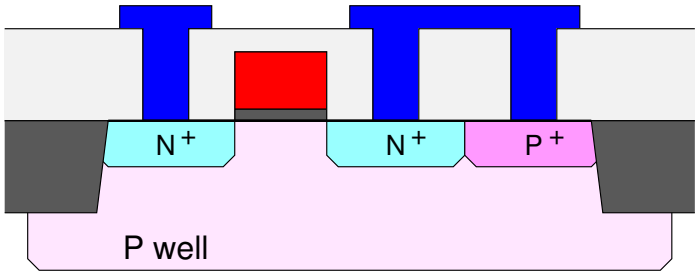
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[1] select **all** options that apply

Question 0



Q0. This is a valid cross-section of:[1]

A. ~~a PMOS transistor for an N-Well Process~~

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. ~~an NMOS transistor for an N-Well Process~~

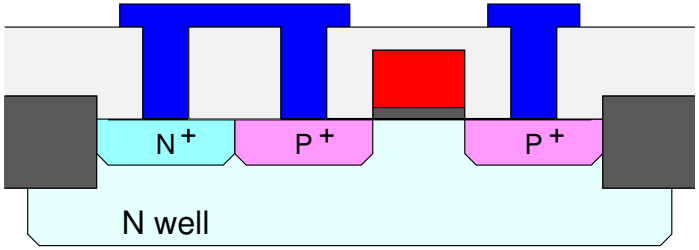
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[1] select **all** options that apply

Question 1



Q1. This is a valid cross-section of:[2]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

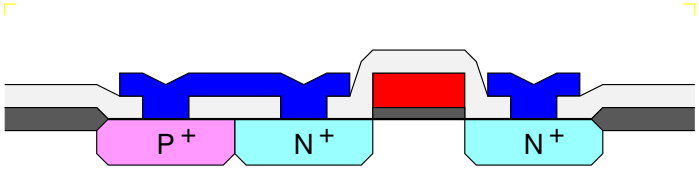
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[2] select **all** options that apply

Question 2



Q2. This is a valid cross-section of:[3]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

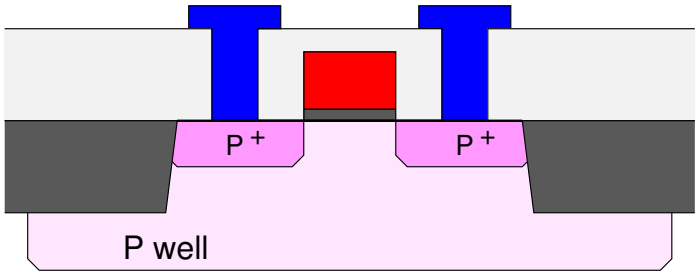
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[3] select **all** options that apply

Question 3



Q3. This is a valid cross-section of:[4]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

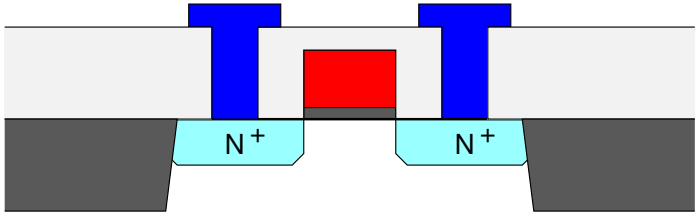
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[4] select **all** options that apply

Question 4



Q4. This is a valid cross-section of:[5]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

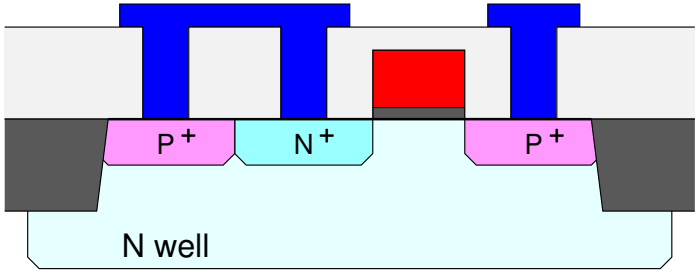
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[5] select **all** options that apply

Question 5



Q5. This is a valid cross-section of:[6]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

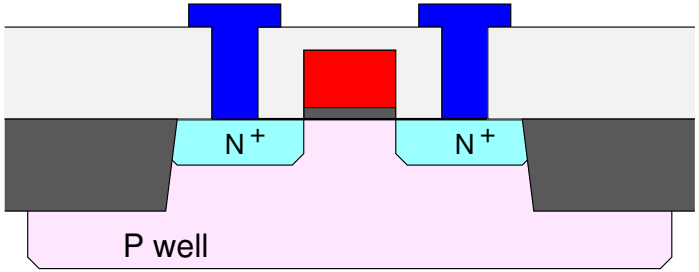
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[6] select **all** options that apply

Question 6



Q6. This is a valid cross-section of:[7]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

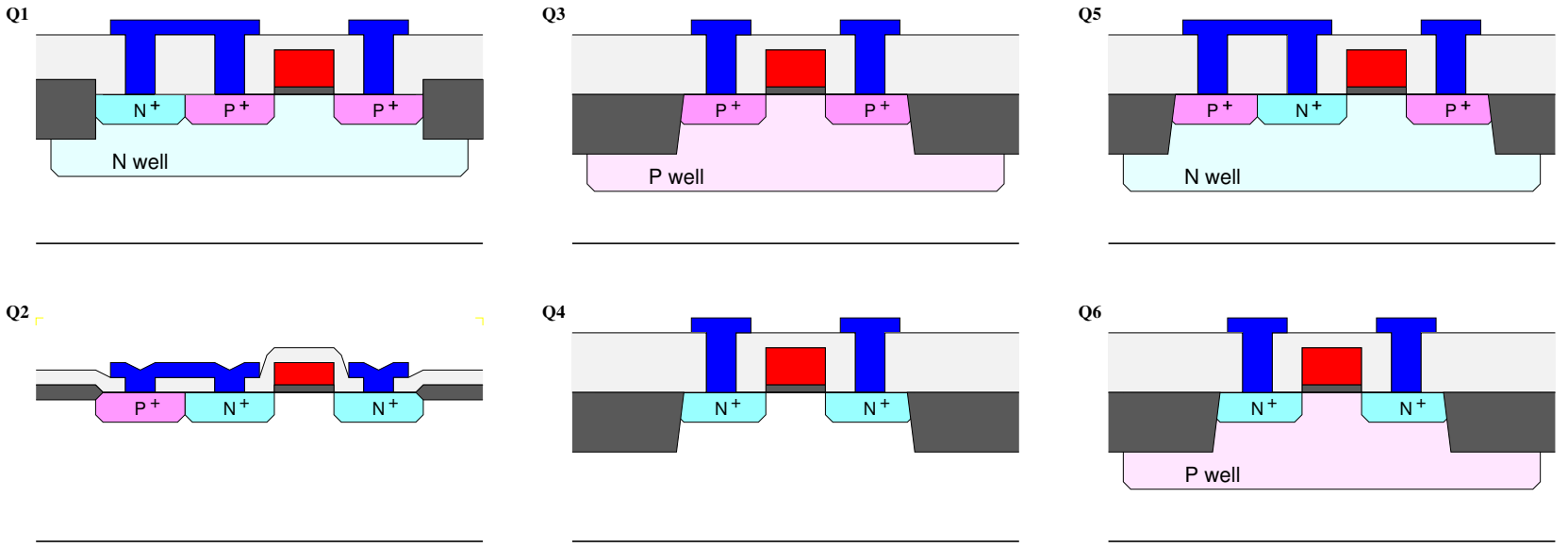
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[7] select **all** options that apply

Questions 1-6



Q1-Q6. This is a valid cross-section of:[8]

A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

[8] select **all** options that apply