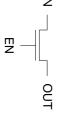
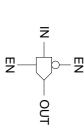
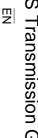


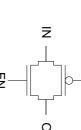


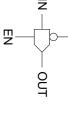
## CMOS Transmission Gate

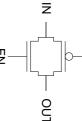












### OUT

Transmission Gate Multiplexor

Distributed Multiplexing

using transmission gates

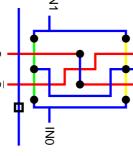
**Z**1

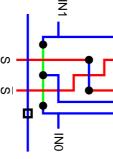
TUO

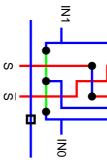
TUO -

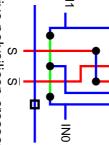
 $\overline{\mathbb{N}}$ 

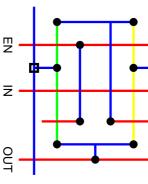
Z -











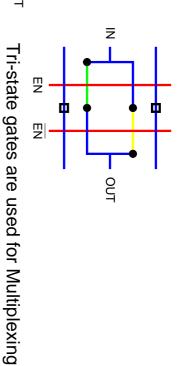
EZ –

Ξ

\_ OUT

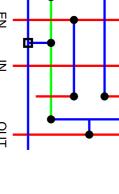
Tri-state Inverter

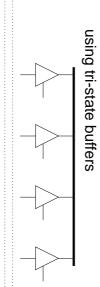
₹



# \*note distictive polysilicon crossover

using tri-state inverters







Tri-state Buffer

TUO-

₹ |

