ELEC3025W1

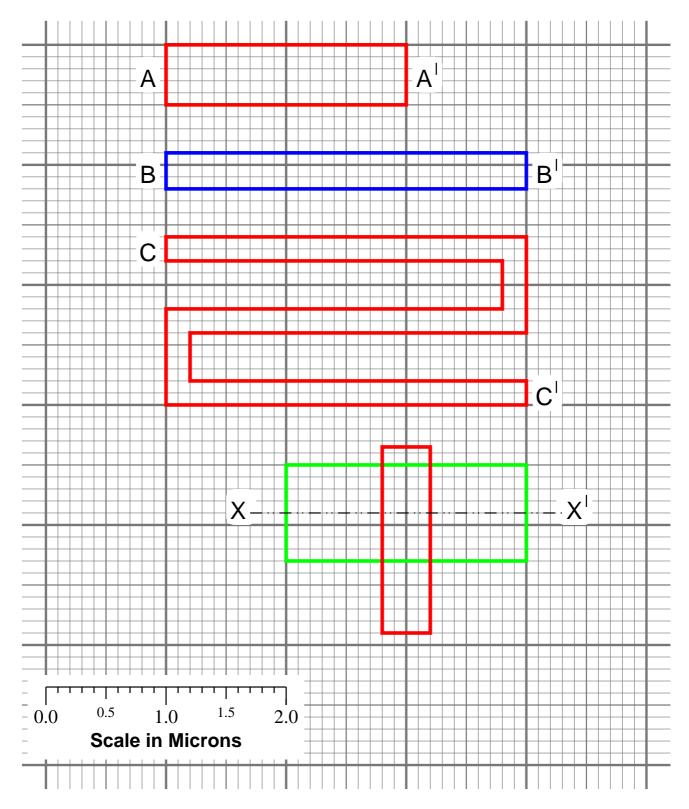


Figure A1: Mask level layout of circuit elements

