

ARM - Xilinx SoC Lab-in-a-Box One day workshops

Agenda

Time	LAB FOLDER	TOPIC
8:30 – 9:00 AM		Registration
9:00 – 9:30 AM		AUP Introduction XUP Introduction
9:30 – 10:00 AM	P2	Introduction to ARM[®] Cortex[®] -M0 processor & DesignStart™
10:00 – 10:30 AM	P3	Overview of AMBA [®] 3 AHB-Lite Protocol
10:30 – 11:00 AM	P4	Artix-7 FPGA Architecture
11:00 – 11:30 AM	P5	Vivado Design Flow
11:30 – 12:30 PM	P6	Lab: Simple AHB Peripheral Design and Integration Simulation
12:30 – 1:30 PM		Lunch
1:30 – 2:30 PM	P6	Lab: Simple AHB Peripheral Design and Integration Implementation
2:30 – 3:15 PM	P7	Lab: Integrating UART & Memory Controller Peripheral
3:30 – 4:15 PM	P8	Lab: Programing in C, Retarget & Interrupts
4:15 – 4:30	P9	Lab: A complete Cortex-M0 based SoC Demo. Snake Game Application
4:30 – 5:00 PM		Conclusion, Call for Action, ARM-Xilinx Lab-in-a-Box